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# Extending fairness expressibility of ECTL<sup>+</sup>: a tree-style one-pass tableau approach (Extended Abstract)

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Temporal logic has become essential for the specification and verification of hardware and software systems. For the specification of the reactive and distributed systems, or, most recently, autonomous systems, the modelling of the possibilities ‘branching’ into the future is essential. Among important properties of these systems, so called fairness properties are important. In the standard formalisation of fairness, operators  $\diamond$  (eventually) and  $\square$  (always) have been used:  $A\diamond\square p$  – ‘ $p$ ’ is true along all computation paths except possibly their finite initial interval, where ‘ $A$ ’ is ‘for all paths’ quantifier, and  $E\square\diamond p$  – ‘ $p$ ’ is true along a computation path at infinitely many states, where ‘ $E$ ’ stands for ‘there exists a path’ quantifier. Branching-time logics (BTL) here give us an appropriate reasoning framework, where the most used class of formalisms are ‘CTL’ (Computation Tree Logic) type logics. CTL itself requires every temporal operator to be preceded by a path quantifier, thus, cannot express fairness. ECTL (Extended CTL) [1] enables simple fairness constraints but not their boolean combinations. ECTL<sup>+</sup> [2] further extends the expressiveness of ECTL allowing boolean combinations of temporal operators and ECTL fairness constraints (but not permitting their nesting). The logic CTL<sup>\*</sup>, often considered as ‘the full branching-time logic’ overcomes these restrictions on expressing fairness. However, CTL<sup>\*</sup> is extremely challenging for the application of any known technique of automated reasoning. Note that, unlike fair CTL [3] which, in tackling fairness, changes the underlying trees to those with ‘fair paths’ only, ECTL and ECTL<sup>+</sup> do not impose these changes.

From another perspective, the literature on fairness constraints, even in the linear-time setting, lacks the analysis of their formulation with the  $\mathcal{U}$  (‘until’) operator. To the best of our knowledge, there are only a few research papers that raise or discuss the problem. For example, [4], introduces the logic LCTL, providing an extension of liveness constraints by the “until” operator. However, LCTL belongs to ‘Fair CTL-type’ logics [5]. ‘Generalised liveness assumptions, which allow to express that the conclusion  $f_2 \mathcal{U} f_3$  of a liveness assumption  $\square(f_1 \Rightarrow (f_2 \mathcal{U} f_3))$  has to be satisfied’ are addressed in [6]. The  $\mathcal{U}$  operator in the formulation of the fairness can also be found in [7] which considers the sequential composition of processes, providing the following example - the composition of processes  $P_1$  and  $P_2$

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‘behaves as  $P1$  until its termination and then behaves as  $P2$ ’. Finally, [8] utilises restricted linear-time fairness constraints with  $\mathcal{U}$  in the linear-time setting. We are not aware of any other analysis of fairness constraints in branching-time setting using the  $\mathcal{U}$  operator and without restricting the underlying logic to be interpreted over the ‘fair’ paths. We bridge this gap, presenting the logic ECTL<sup>#</sup> (we use # to indicate some restrictions on concatenations of the modalities and their boolean combinations). It is weaker than CTL<sup>\*</sup> but extends ECTL<sup>+</sup> by allowing the combinations  $\Box(A \mathcal{U} B)$  or  $A \mathcal{U} \Box B$ , referred to as modalities  $\Box \mathcal{U}$  and  $\mathcal{U} \Box$ . This enables the formulation of stronger fairness constraints in the branching-time setting. The fairness constraint  $A(p \mathcal{U} \Box q)$  reads as ‘ $q$  is true along all paths of the computation except possibly their finite initial interval, where  $p$  is true’. For example, in specifying the situation when a user of a system, changing passwords, cannot repeat any old password, the following property should hold:  $A((password = pw) \mathcal{U} \Box (password \neq pw))$  provided that  $pw$  is ‘the current password’.

$\mathcal{B}(\mathcal{U}, \circ)$ (CTL) extensions	$E(\Box \Diamond q)$	$E(\Box \Diamond q \wedge \Box \Diamond r)$	$A((p \mathcal{U} \Box q) \vee (s \mathcal{U} \Box \neg r))$	$A \Diamond (\circ p \wedge E \circ \neg p)$
$\mathcal{B}(\mathcal{U}, \circ, \Box \Diamond)$ (ECTL)	✓	X	X	X
$\mathcal{B}^+(\mathcal{U}, \circ, \Box \Diamond)$ (ECTL <sup>+</sup> )	✓	✓	X	X
$\mathcal{B}^+(\mathcal{U}, \circ, \mathcal{U} \Box)$ (ECTL <sup>#</sup> )	✓	✓	✓	X
$\mathcal{B}^*(\mathcal{U}, \circ)$ (CTL <sup>*</sup> )	✓	✓	✓	✓

Figure 1: Classification of CTL-type logics and their expressiveness

Figure 1 places our logic in the hierarchy of BTL representing their expressiveness: logics are classified by using ‘ $\mathcal{B}$ ’ for ‘Branching’, followed by the set of only allowed modalities as parameters;  $\mathcal{B}^+$  indicates admissible boolean combinations of the modalities and  $\mathcal{B}^*$  reflects ‘no restrictions’ in either concatenations of the modalities or boolean combinations between them.<sup>1</sup> Thus,  $\mathcal{B}(\mathcal{U}, \circ)$  denotes the logic CTL. In this hierarchy ECTL<sup>#</sup> is  $\mathcal{B}^+(\mathcal{U}, \circ, \mathcal{U} \Box)$ .

For linear-time logics which deal with fairness in the linear-time setting, one-pass and two-pass tableau methods have been developed. In the repository of the CTL-type branching-time setting, the well-known logics ECTL and ECTL<sup>+</sup> were developed to explicitly deal with fairness. However, due to the syntactical restrictions, these logics can only express restricted versions of fairness. The logic CTL<sup>\*</sup>, often considered as ‘the full branching-time logic’ overcomes these restrictions on expressing fairness. However, CTL<sup>\*</sup> is extremely challenging for the application of verification techniques, and the tableau technique, in particular. For example, there is no one-pass tableau construction for CTL<sup>\*</sup>, while one-pass tableau has an additional benefit enabling the formulation of dual sequent calculi that are often treated as more ‘natural’ being more friendly for human understanding. These two considerations lead to the following problem - are there logics that have richer expressiveness than ECTL<sup>+</sup>, allowing the formulation of a new range of fairness constraints with ‘until’ operator, yet ‘simpler’ than CTL<sup>\*</sup>, and for which a one-pass tableau can be developed? Here we give a positive answer to this question, introducing a sub-logic of CTL<sup>\*</sup> called ECTL<sup>#</sup>, its tree-style one-pass tableau, and an algorithm for obtaining a systematic tableau, for any given admissible branching-time formulae. We prove the termination, soundness and completeness of the method. As tree-shaped one-pass tableaux are well suited for the automation and are amenable for the implementation and for the formulation of sequent calculi. Our results also open a prospect of relevant developments of the automation and implementation of the tableau method for

<sup>1</sup>This notation goes back to [9], here we use its nice tuning by Nicolas Markey in [10]. In the last column we use a short CTL<sup>\*</sup> formula  $A \Diamond (\circ p \wedge E \circ \neg p)$ , not expressible by weaker logics. We found this formula indicative for CTL<sup>\*</sup> as its validity is directly linked to the limit closure property [9].

ECTL<sup>#</sup>, and of a dual sequent calculi.

We present a tree-style one-pass tableau for this logic continuing the analogous developments in linear-time case [11, 12] and for CTL [11]. An indicative feature of this approach is a context-based tableau technique. To the best of our knowledge, the context-based tableau has not been extended to BTL more expressive than CTL, though for them different other kinds of tableaux exist. In particular, [13] presents a tableau based decision procedure for CTL<sup>\*</sup>, which would definitely cover ECTL<sup>#</sup> as a sublogic of CTL<sup>\*</sup>. However, this tableau method is (unavoidably) complicated. For example, it utilises ‘global conditions on infinite branches’ to be checked by the automata-theoretic approach. Though such complications may be well justified by the complexity of CTL<sup>\*</sup>, aiming at a weaker logic, we would benefit by reducing the complications to the minimum. Also, a distinctive feature of the tableau method in [13] is the control of loops, specifically, of so called ‘bad loops’. While it looks necessary for this technique, we would like to avoid similar complications for a simpler logic, ECTL<sup>#</sup>. Moreover, due to the essential use of the notion of ‘context’ (see §??) our tableau rules only produce ‘good loops’. Tree-style one-pass tableaux (without additional procedures for checking meta-logical properties) have dual (cut-free) sequent calculi, see [12], enabling the construction of human-understandable proofs. In addition, these tableaux are well suited for the automation and are amenable for the implementation.<sup>2</sup> Our tableau is effectively an AND-OR tree where nodes are labelled by sets of state (see the definitions in §??) formulae. There are difficult cases of ECTL<sup>#</sup> formulae that appear due to the enriched syntax: disjunctions of formulae in the scope of the A quantifier and conjunctions of formulae in the scope of the E quantifier. To tackle these cases, in addition to  $\alpha - \beta$  rules, that are standard to the tableaux, we use novel  $\beta^+$ -rules which use the context to force the eventualities to be fulfilled as soon as possible.

## References

- [1] E. A. Emerson, J. Y. Halpern, Decision procedures and expressiveness in the temporal logic of branching time, *Journal of Computer and System Sciences* 30 (1) (1985) 1–24. doi:10.1016/0022-0000(85)90001-7.
- [2] E. A. Emerson, J. Y. Halpern, Sometimes and not never revisited: On branching versus linear time temporal logic, *J. ACM* 33 (1) (1986) 151–178. doi:10.1145/4904.4999.
- [3] E. M. Clarke, E. A. Emerson, A. P. Sistla, Automatic verification of finite-state concurrent systems using temporal logic specifications, *ACM Trans. Program. Lang. Syst.* 8 (2) (1986) 244–263. doi:10.1145/5397.5399.
- [4] B. Josko, Model checking of ctl formulae under liveness assumptions, in: T. Ottmann (Ed.), *Automata, Languages and Programming, 14th International Colloquium*, Springer-Verlag Berlin Heidelberg, Karlsruhe, Federal Republic of Germany, 1987, pp. 5–24. doi:10.1007/3-540-18088-5.
- [5] E. A. Emerson, C.-L. Lei, Temporal reasoning under generalized fairness constraints, in: M. B., V.-N. G. (Eds.), *STACS 1986, Lecture Notes in Computer Science*, vol 210, Springer-Verlag Berlin Heidelberg, Karlsruhe, Federal Republic of Germany, 1986, pp. 21–37. doi:10.1007/3-540-16078-7\_62.
- [6] T. Berg, H. Raffelt, Model checking, in: M. Broy, B. Jonsson, J.-P. Katoen, M. Leucker, A. Pretschner (Eds.), *Model-Based Testing of Reactive Systems*, Springer-Verlag, Berlin Heidelberg, 2005, pp. 557–603. doi:10.1007/b137241.  
URL <https://www.springer.com/gb/book/9783540262787>
- [7] J. Sun, Y. Liu, J. S. Dong, H. H. Wang, Specifying and verifying event-based fairness enhanced systems, in: S. Liu, T. Maibaum, K. Araki (Eds.), *Formal Methods and Software Engineering: 10th International Confer-*

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<sup>2</sup>An excellent survey of the seminal tableau techniques for temporal logics can be found in [14].

- ence on Formal Engineering Methods ICFEM 2008, Springer Science and Business Media, Kitakyushu-City, Japan, 2008, pp. 5–24. doi:10.1007/978-3-540-88194-0.
- [8] J. Kretinsky, R. Ledesma Garza, Rabinizer 2: Small deterministic automata for LTL\GU, in: Automated Technology for Verification and Analysis - 11th International Symposium, ATVA 2013, Springer, Heidelberg Dordrecht London New York, 2013, pp. 446–450. doi:10.1007/978-3-319-02444-8\_32.
- [9] E. A. Emerson, Temporal and modal logic, in: J. van Leeuwen (Ed.), Handbook of Theoretical Computer Science (Vol. B), MIT Press, Cambridge, USA, 1990, pp. 995–1072.  
URL <http://dl.acm.org/citation.cfm?id=114891.114907>
- [10] N. Markey, Temporal logics, course notes, Master Parisien de Recherche en Informatique, Paris, France (2013).  
URL <http://www.lsv.ens-cachan.fr/Publis/PAPERS/PDF/NM-coursTL13.pdf>
- [11] K. Brännler, M. Lange, Cut-free sequent systems for temporal logic, *Journal of Logic and Algebraic Programming* 76 (2) (2008) 216–225. doi:10.1016/j.jlap.2008.02.004.
- [12] J. Gaintzarain, M. Hermo, P. Lucio, M. Navarro, F. Orejas, Dual systems of tableaux and sequents for PLTL, *Journal of Logic and Algebraic Programming* 78 (8) (2009) 701–722. doi:10.1016/j.jlap.2009.05.001.
- [13] M. Reynolds, A tableau for CTL<sup>\*</sup>, in: A. Cavalcanti, D. Dams (Eds.), FM 2009: Formal Methods, Second World Congress, Eindhoven, The Netherlands, November 2-6, 2009. Proceedings, Vol. 5850 of Lecture Notes in Computer Science, Springer, 2009, pp. 403–418. doi:10.1007/978-3-642-05089-3\_26.
- [14] R. Gore, Tableau methods for modal and temporal logics, in: M. D’Agostino, D. M. Dov Gabbay, R. Hähnle, J. Posegga (Eds.), Handbook of Tableau Methods, Springer, Netherlands, Dordrecht, 1999, pp. 297–396. doi:10.1007/978-94-017-1754-0\_6.
- [15] R. S. Streett, E. A. Emerson, The propositional mu-calculus is elementary, in: J. Paredaens (Ed.), Automata, Languages and Programming, 11th Colloquium, Antwerp, Belgium, July 16-20, 1984, Proceedings, Vol. 172 of Lecture Notes in Computer Science, Springer, 1984, pp. 465–472. doi:10.1007/3-540-13345-3\_43.